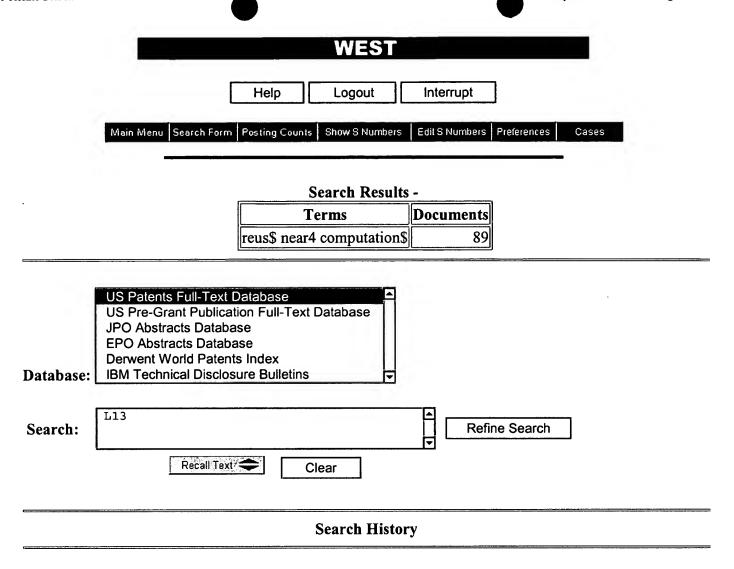
Help Logout Interrupt Main Menu Search Form Posting Counts Show S Numbers Edit S Numbers Preferences Cases Search Results Terms Documents L11 and (execut\$ near4 (order\$ or sequence\$)) 15

	US Patents Full-Text Database US Pre-Grant Publication Full-Text Database JPO Abstracts Database EPO Abstracts Database			
	Derwent World Patents Index			
Database:	IBM Technical Disclosure Bulletins	巨		
Search:	L12		Refine Search	
	Recall Text Clear			

Search History

DATE: Wednesday, May 14, 2003 Printable Copy Create Case

Set Name Query		Hit Count	Set Name
side by side			result set
DB=US	SPT; PLUR=YES; OP=ADJ		
<u>L12</u>	L11 and (execut\$ near4 (order\$ or sequence\$))	15	<u>L12</u>
<u>L11</u>	15 and ((input\$ near4 register\$) and (output\$ near4 register\$))	28	<u>L11</u>
<u>L10</u>	L9 and (order\$ or sequenc\$) near5 execut\$	0	<u>L10</u>
<u>L9</u>	L8 and execut\$	1	<u>L9</u>
<u>L8</u>	5175863.pn.	1	<u>L8</u>
<u>L7</u>	L6 and ((Input\$ or source\$)near4 operand\$)	3	<u>L7</u>
<u>L6</u>	L5 and register\$	55	<u>L6</u>
<u>L5</u>	((reus\$ near4 compute) or (reus\$ near4 computation\$))	109	<u>L5</u>
<u>L4</u>	L3 and (live in and live out) and register\$	0	<u>L4</u>
<u>L3</u>	(reus\$ near4 comput\$)	450	<u>L3</u>
<u>L2</u>	L1 and map\$ same (many to one or many-to-one)	2	<u>L2</u>
<u>L1</u>	(reus\$ near4 comput\$)	450	<u>L1</u>



DATE: Wednesday, May 14, 2003 Printable Copy Create Case

Set Name Query side by side		Hit Count Set Name result set	
-	SPT; PLUR=YES; OP=ADJ		
<u>L13</u>	reus\$ near4 computation\$	89	<u>L13</u>
<u>L12</u>	L11 and (execut\$ near4 (order\$ or sequence\$))	15	<u>L12</u>
<u>L11</u>	15 and ((input\$ near4 register\$) and (output\$ near4 register\$))	28	<u>L11</u>
<u>L10</u>	L9 and (order\$ or sequenc\$) near5 execut\$	0	<u>L10</u>
<u>L9</u> .	L8 and execut\$	1	<u>L9</u>
<u>L8</u>	5175863.pn.	1	<u>L8</u>
<u>L7</u>	L6 and ((Input\$ or source\$)near4 operand\$)	3	<u>L7</u>
<u>L6</u>	L5 and register\$	55	<u>L6</u>
<u>L5</u>	((reus\$ near4 compute) or (reus\$ near4 computation\$))	109	<u>L5</u>
<u>L4</u>	L3 and (live in and live out) and register\$	0	<u>L4</u>
<u>L3</u>	(reus\$ near4 comput\$)	450	<u>L3</u>
<u>L2</u>	L1 and map\$ same (many to one or many-to-one)	2	<u>L2</u>
<u>L1</u>	(reus\$ near4 comput\$)	450	<u>L1</u>

END OF SEARCH HISTORY